

Atomically thin semiconducting channels for future nano-electronics

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Using atomic-scale thin film of metal chalcogenide layered material, we have developed semiconducting channel for future electronics. We investigated transport properties, particularly scattering property of carrier transport. In this transport experiment, it is found that the carrier scattering from interfacial Coulomb impurities is greatly intensified in extremely thinned channels, resulting from shortened interaction distance between impurities and carriers.

Thus, we fabricated MoS₂ field-effect transistors on crystalline hexagonal boron nitride (h-BN) and SiO₂ substrates. Temperature dependence of these transistors shows distinct weak temperature dependence of the MoS₂ devices on h-BN substrate. At the room temperature, mobility enhancement and reduced interface trap density of the single and bilayer MoS₂ devices on h-BN substrate further indicate that reducing substrate traps is crucial for enhancing the mobility in atomically thin MoS₂ devices. More detail of carrier scattering in the atomic-scale thin channel will be discussed.

Furthermore, we have developed field effect transistor using high-k flak dielectric with layered structure.

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